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V Reddy, S Swanson, B Segeike, KA Kantardjieff, JC ... - Acta Crystallographica Section D: Biological Crystallography, 2003 - dx.doi.org

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MA Blake, CB Ford III, P Mak - US Patent 5,752,264, 1998 - Google Patents

... US005752264A [ii] Patent Number: [45] Date of Patent: 5,752,264 May 12, 1998 [54]

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R Bealkowski, S Dhawan, KC Hinz, PM Thomsen - US Patent 6,378,027, 2002 - Google Patents

... system of claim 9 wherein: said **processor** array includes a plurality of **processor clusters**; and said program instructions further quiesce, **remove** from said ...

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RW Horst, TC Inc, CA Cupertino - IEEE Micro, 1995 - [ieeexplore.ieee.org](#)

... Figure 2. CPU **cluster** connected with the TNet system area ... If there is no way to **remove** these stale ... The service **processor** can modify routing tables to configure ...

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[Partitioning strategies for structured multiblock grids](#)

J Rantakokko - Parallel Computing, 2000 - Elsevier

... Thus, finding  $\min_i (p * i / (p - 1))$  and **removing** a **processor** from this **cluster** increases the load imbalance  $\Lambda$  as little as possible. ...

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S Schwartz, WJ Kent, A Smit, Z Zhang, R Baertsch, ... - Genome Research, 2003 - [genome.cshlp.org](#)

... We now **remove** recent repeat elements, run BLASTZ, then adjust ... software available on the 1024 CPU **cluster** that we use doesn't support **processor** affinity of ...

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JB Weissman - Journal of Parallel and Distributed Computing, 2002 - [www-users.cs.umn.edu](#)

... on SPMD data parallel applications in shared **clusters**, we implemented a ... non-block, migrate-stream, migrate-ghost, add-**processor**, **remove-processor**, and dynamic ...

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D Lenoski, J Laudon, T Joe, D Nakahira, L Stevens, ... - Computer Architecture, 1992. Proceedings., The 19th Annual ..., 1992 - [ieeexplore.ieee.org](#)

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